Section and Group No:

Name:

Signature:

EXP #5: IMPLEMENTATION OF FULL ADDER WITH TWO HALF ADDERS AND AN OR GATE

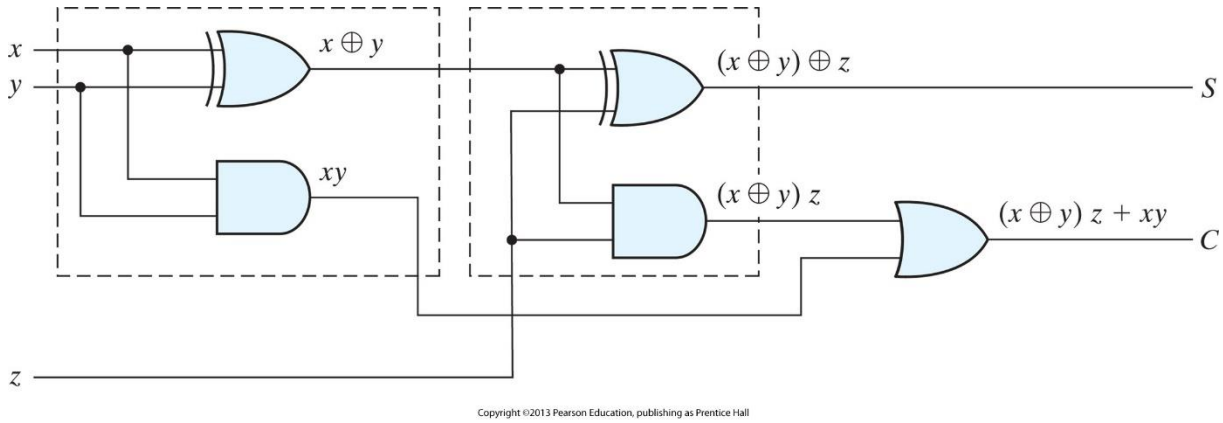
Components that will be ensured by the students.

Objective(s):

In this experiment, you will design, construct, and test a full adder with two half adders and an OR gate. (Section 4.5 of your textbook).

Preliminary Work:

Obtain the truth table for the circuit given in Figure 1.



Copyright ©2013 Pearson Education, publishing as Prentice Hall

Figure 1

x	*y*	*z*	*S*	*C*

Table 1

Laboratory Work:

Construct and test the circuit given in Figure 1 and fill the Table 2. Compare your results with the truth table you prepared at your preliminary work.

x	***y***	***z***	***S***	***C***

Table 2