Section and Group No:

Name:

Signature:

EXP #4: COMBINATIONAL CIRCUITS

Equipment will be ensured by the students: XOR gates

Objective(s):

In this experiment, you will design, construct, and test a parity generator. (Section 3.8 of your textbook).

Preliminary Work:

Design a circuit that generates an even parity bit from four message bits. Obtain the truth table and draw the circuit diagram using XOR gates.

Truth Table:

Circuit Diagram with XOR:

Laboratory Work:

Construct the circuit you implemented at your preliminary work and test it for proper operation by verifying the given conditions.